

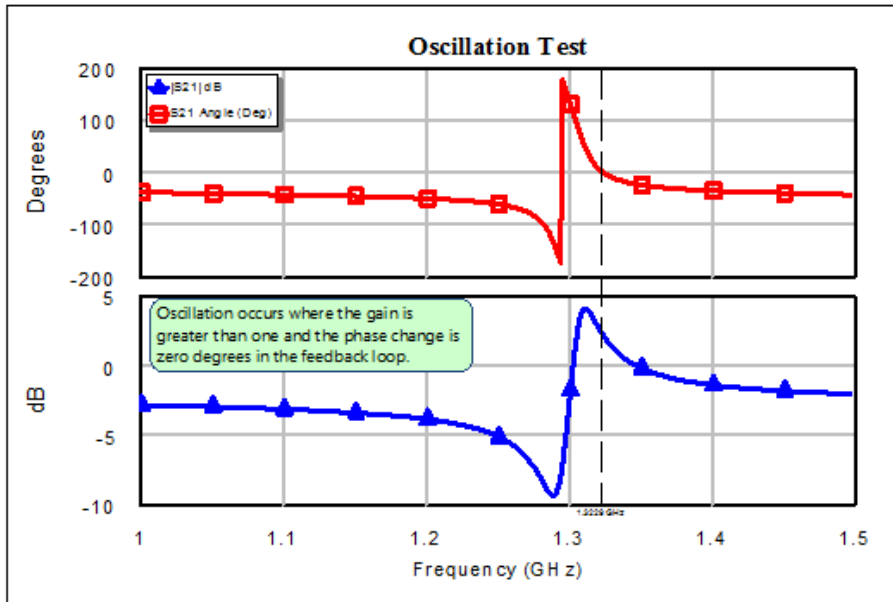
Oscillator_Linear_Loop_Gain_Analysis

This test bench project shows a loop gain analysis to determine the linear oscillation condition using OSCTEST element.

This test bench includes:

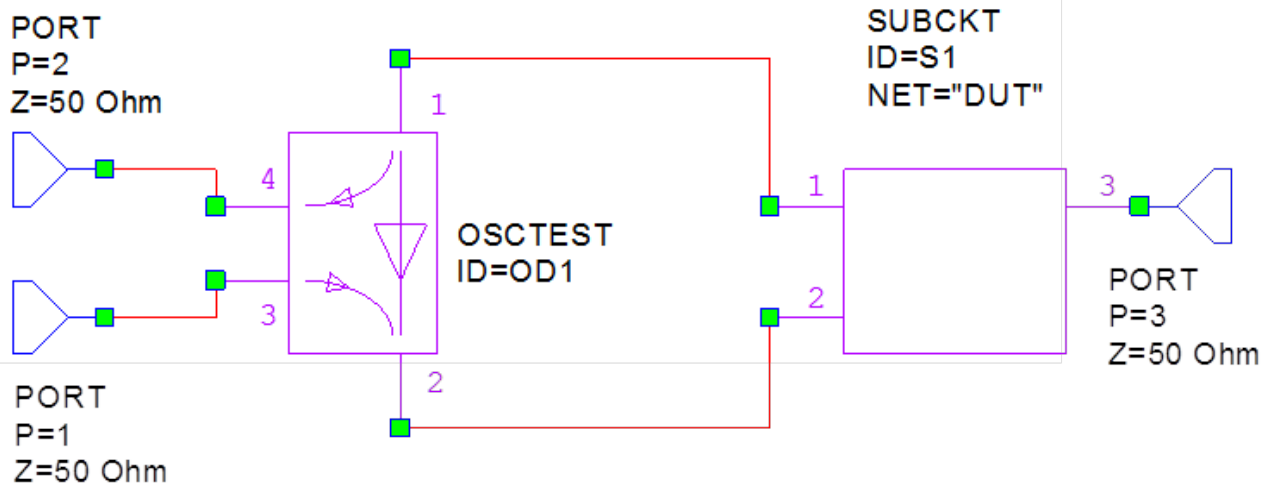
Schematics:

- **Oscillator_Linear_Loop_Gain_Analysis_Display** - displays the embedded graph



- **Oscillator_Linear_Loop_Gain_Analysis** - the test bench setup. Includes notes on where to change settings specific to your design

- Set Frequency On Schematic Options Right click on the schematic Select "Options". On the "Frequencies" tab, enter the desired frequency range. These are linear frequencies and therefore should cover a closely spaced range of frequencies.
- 2. Add ports to your Feedback Oscillator in the feedback path of your oscillator, place ports to hook in the OSCTEST in the higher level schematic. Make sure that the orientation of the OSCTEST is correct. The arrow should be pointing into the transistor. See the OSCTEST element help is needed.



Graph

- [Oscillation Test](#)

Project Folders

- [Oscillator_Linear_Loop_Gain_Analysis](#) - Folder containing all the schematics, graphs and equations for this project.

Importing the Test Bench

If you are unable to import the test bench automatically by clicking the Import test bench button above, click [Here](#) to do that manually.