

DigitalAM_BER

Where To Find This Example

Select **Help > Open Examples...** from the menus and type either the example name listed above or one of the keywords below.

Or in Version 13 or higher you can open the project directly from this page using this button. Make sure to select the **Enable Guided Help** before clicking this button.

[Open Install Example](#)

Design Notes

Digital AM System

This project demonstrates a system that uses conventional AM modulation for transmission of digital data.

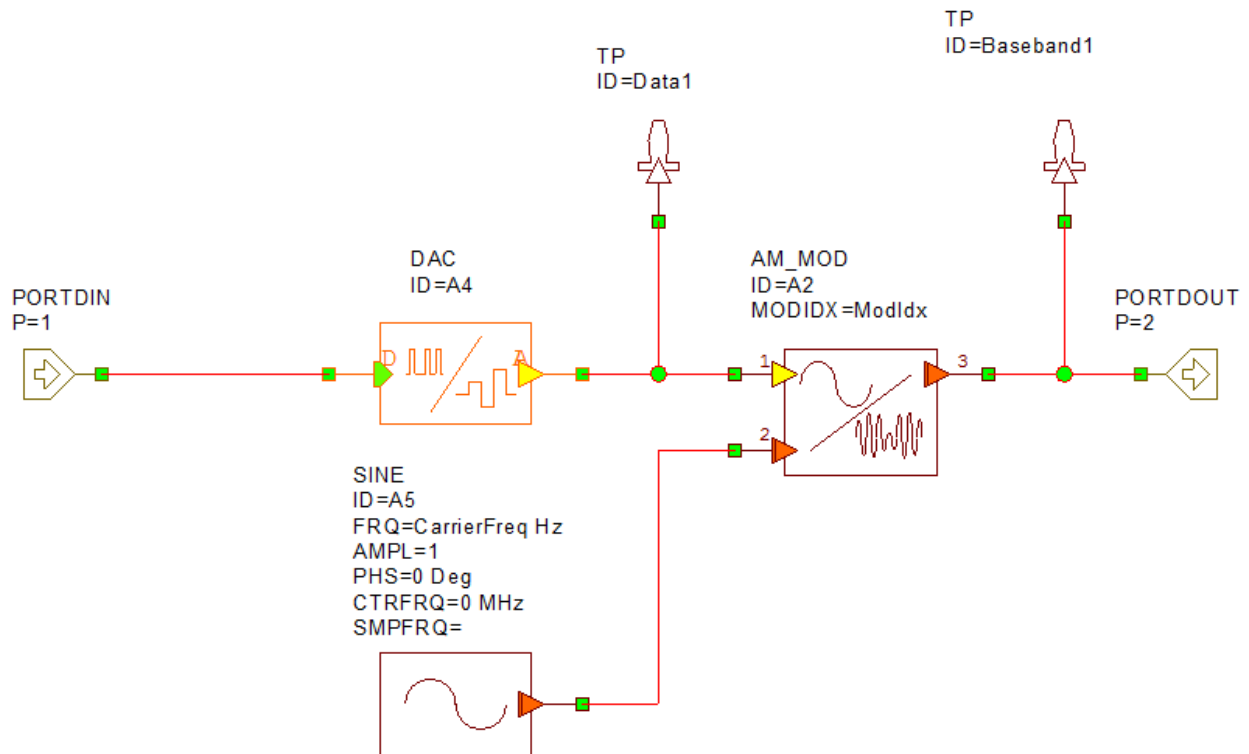
The "Flat AM" system diagram implements the digital AM transmitter and receiver out of discrete VSS components.

The "Nested AM" system diagram uses DIG_AM_TX and DIG_AM_RX blocks for the digital AM transmitter and receiver, respectively. Subcircuit "AM Modulator" has the same internal structure as DIG_AM_TX and subcircuit "AM Demodulator" the same as DIG_AM_RX.

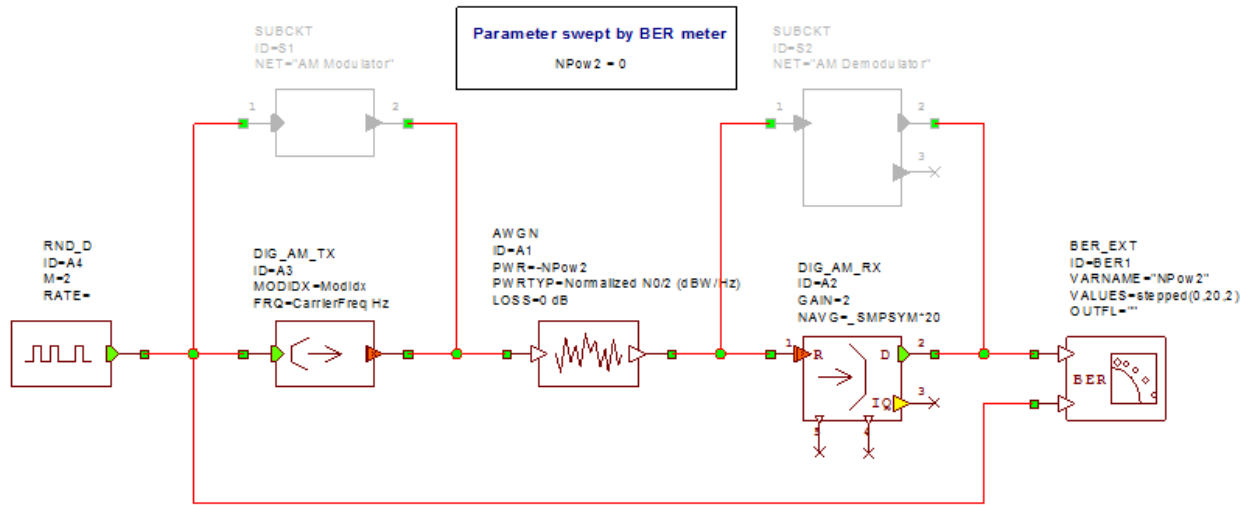
Error rate at the receiver is calculated using BER blocks and displayed in the BER graph.

The carrier frequency and AM modulation index are set in the "Global Definitions" window. You can tune the modulation index by clicking on the "Tune" button and sliding the ruler to the desired level. Its effect on the BER can be observed.

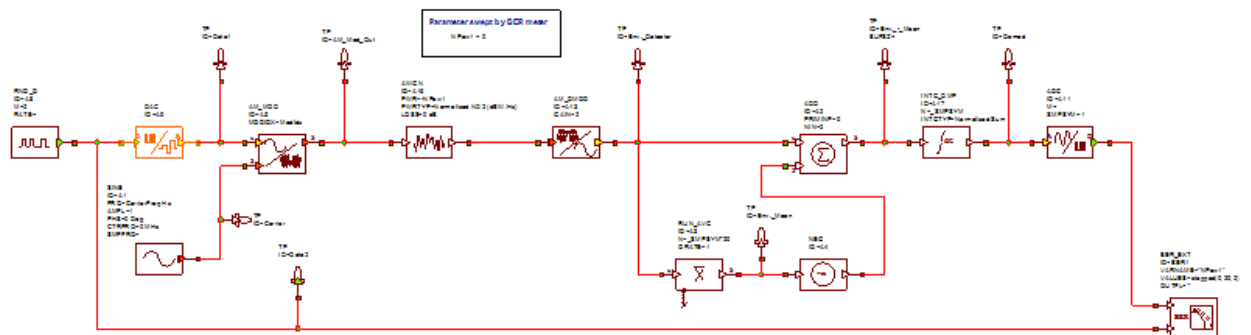
System Diagram - AM Modulator



System Diagram - Nested AM



System Diagram - Flat AM



System Diagram - AM Demodulator

Graph - BER

